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May 13, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant:

Nayon Tomsio, et al.

Title:

RESISTIVE VIAS FOR CONTROLLING IMPEDANCE AND TERMINATING TO

SIGNALS AT THE PACKAGE LEVEL

Application No.:

10/028,814

December 20, 2001

Atty. Docket No.: 004-6459

Client Ref. No.: P6459

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

$\boxtimes$	Response to Non-Final Action (15 p	age(s))		
	Petition for Extension of Time (	month) (	page(s))	
	Information Disclosure Statement (	page(s)),	including PTO F	orm 1449
	( page(s)), and copies of	reference(s)	_	
	Other: ( page(s))			
$\boxtimes$	A Return Postcard and this sheet in a	luplicate		

The Total Fee has been calculated as shown below:

	Claims After Amendment	Claims Previously Paid	Extra Claims	Fees			
Total Claims	34	- 20 =	14 x \$18.00 =	252.00			
Independent Claims	8	- 3 =	5 x \$84.00 =	420.00			
Additional Claims Fee	\$ 672.00						
Fee For Extension Of Time							
Other Fees: ( )							
TOTAL FEE DUE:				\$ 672.00			

		Applicant is,	or	has	established	status	as,	a	small	entity.	•
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 $\boxtimes$ A check is enclosed for the Total Fee shown above.

Please charge the Total Fee shown above to Deposit Account 50-0631.

The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 or  $\boxtimes$ 1.17 which may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.

Commissioner for Patents May 13, 2003 RE: 10/028,814

Page 2 of 2

CERTIFICATE OF MAILING OR TRANSMISSION	<u>)N</u>
I hereby certify that, on the date shown below, this correspondence is being	
deposited with the US Postal Service with sufficient po as first class mail, in an envelope addressed to Commis for Patents, P.O. Box 1450, Alexandria, VA 22313-145	sioner
facsimile transmitted to the US Patent and Trademark (	Office.
Date	

EXPRESS MAIL LABEL: EV 335 378 754 US

Respectfully submitted,

Andrew C. Graham, Reg. No. 36,531

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Nayon Tomsio, et al.

Title:

RESISTIVE VIAS FOR CONTROLLING IMPEDANCE AND

TERMINATING I/O SIGNALS AT THE PACKAGE LEVEL

Application No.: 10/028,814

Filed:

December 20, 2001

Examiner:

V. A. Mandala Jr.

Group Art Unit:

2826

Atty. Docket No.: 004-6459

May 13, 2003

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO NON-FINAL OFFICE ACTION

This paper is responsive to the non-final Office action dated February 13, 2003, having a shortened statutory period for response set to expire May 13, 2003. In light of the Amendments and/or Remarks herein, further examination is requested.

This paper is filed in accordance with the revised format described in "Amendments in a Revised Format Now Permitted," 1267 Off. Gaz. Pat. Office 106 (Feb. 25, 2003), which notice waives the provisions of 37 C.F.R. § 1. 121(a), (b), (c) and (d) for amendments to the claims. specifications, and drawings where the amendments comply with the revised amendment format.

Please amend the above-identified application as follows:

05/15/2003 DENMANUI 00000042 10028814

01 FC:1201 02 FC:1202 420.00 OP 252.00 OP

- 1 -